

FIG. 1
PRIOR ART

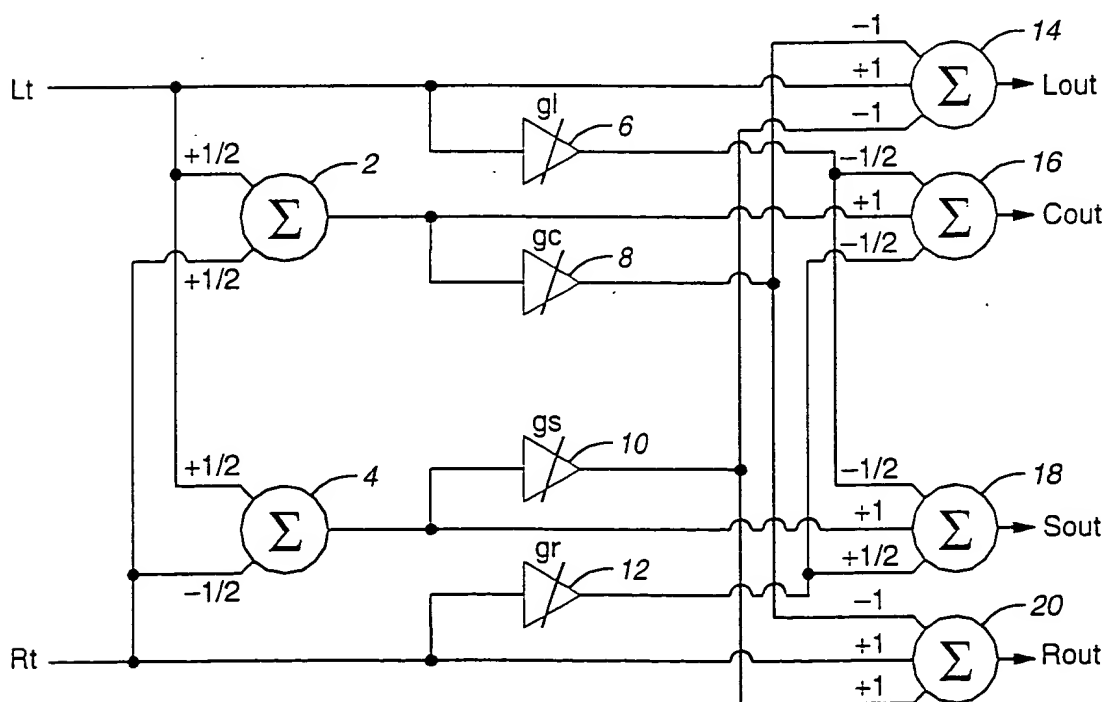


FIG. 2
PRIOR ART

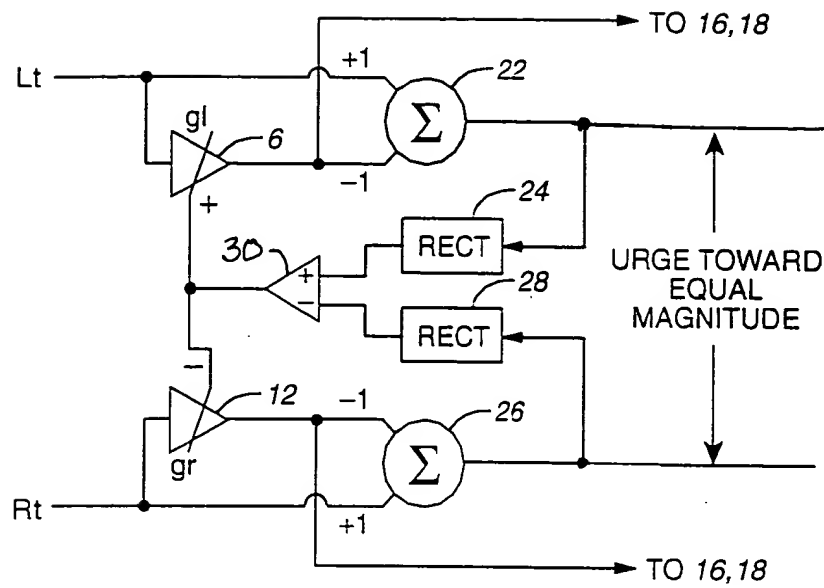


FIG. 3

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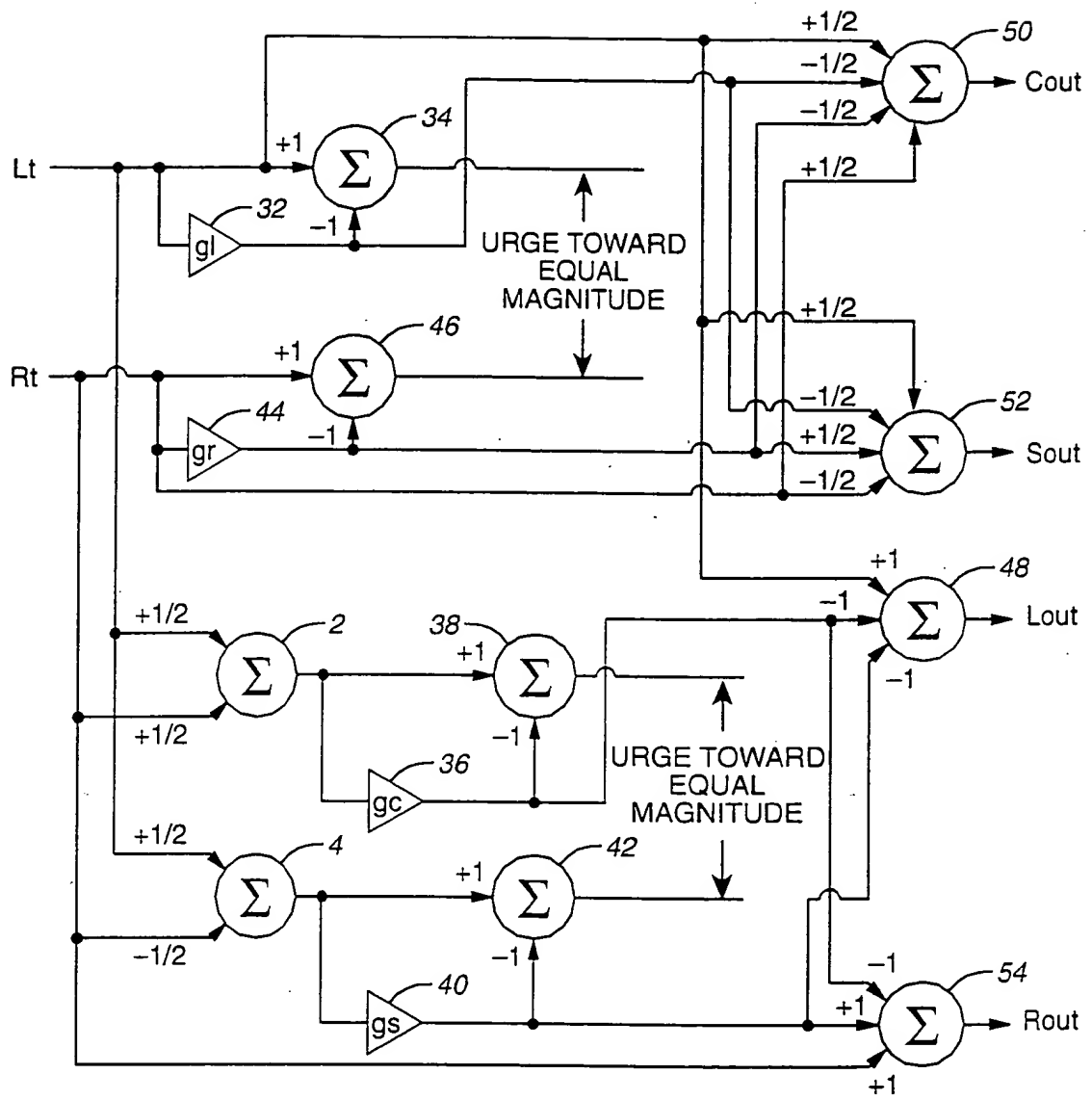


FIG._4

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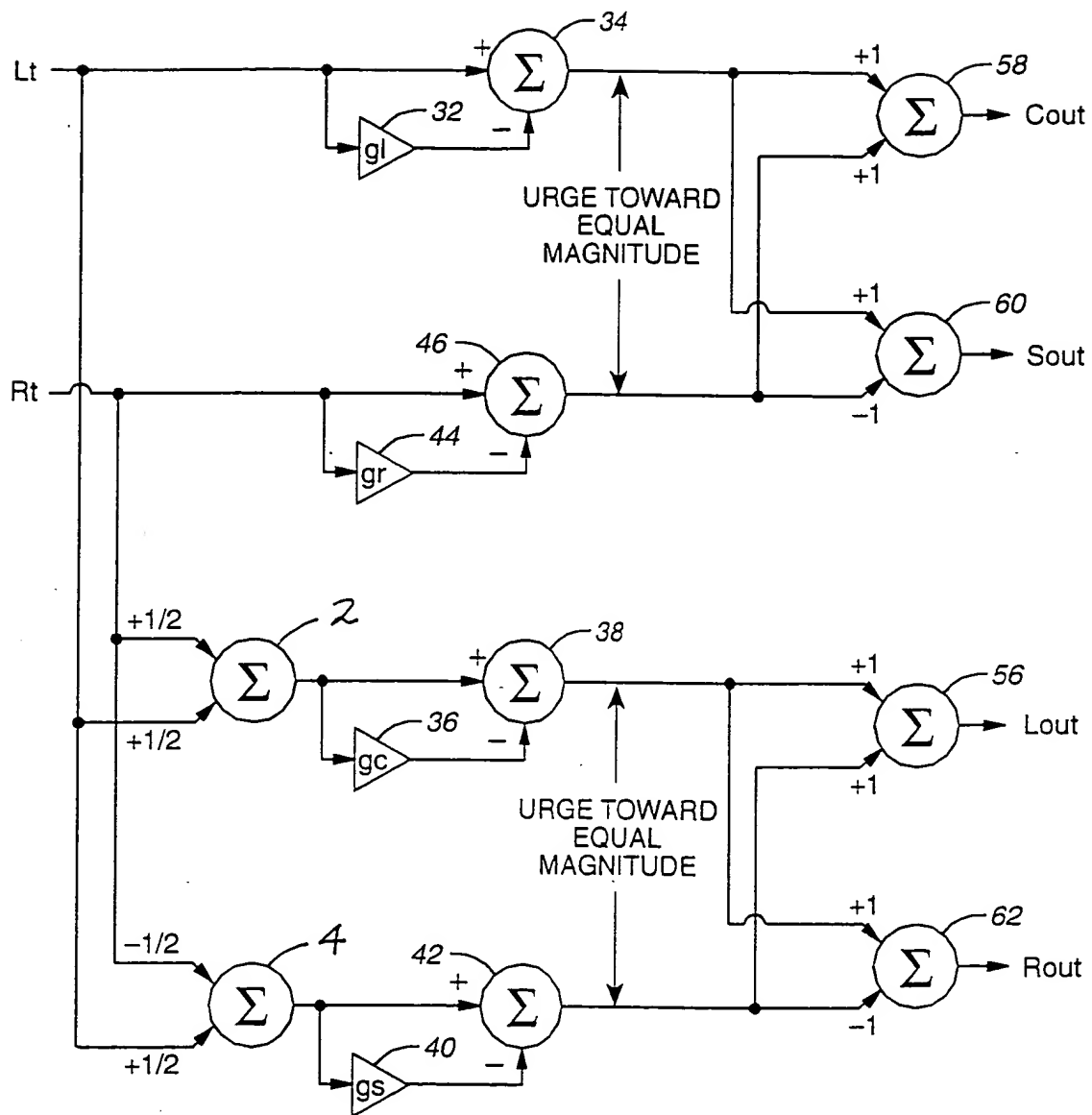


FIG._5

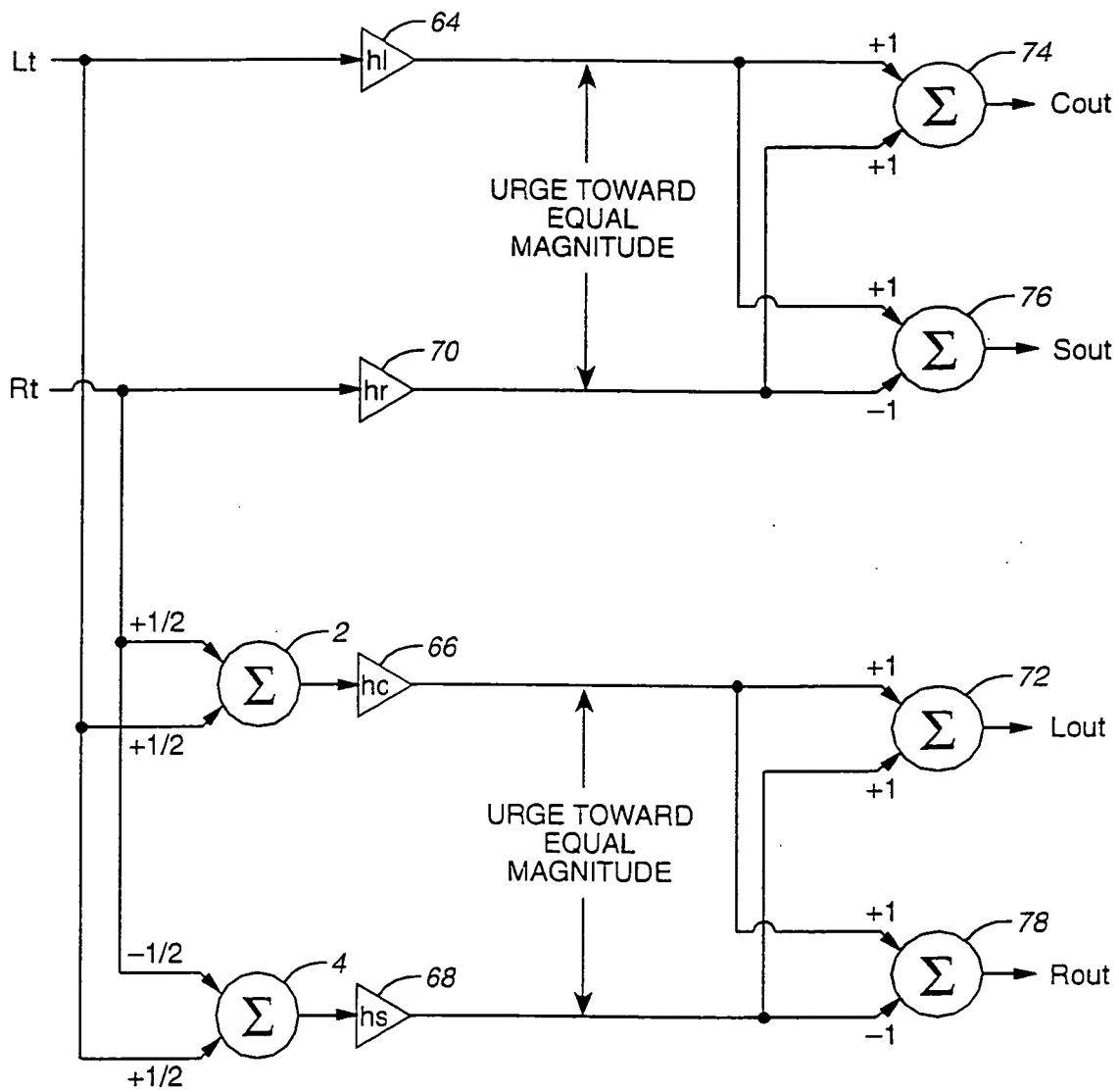


FIG._6

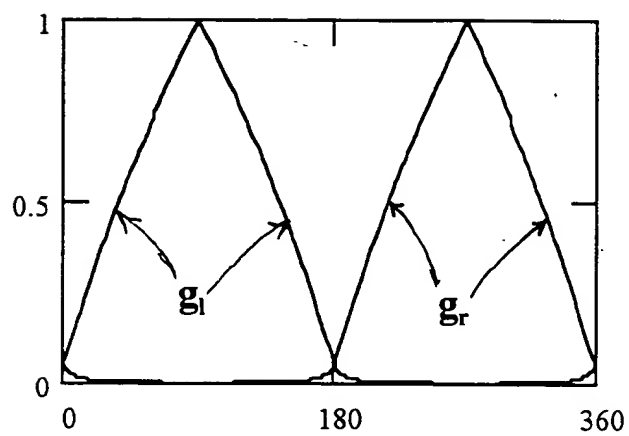


FIG. 7

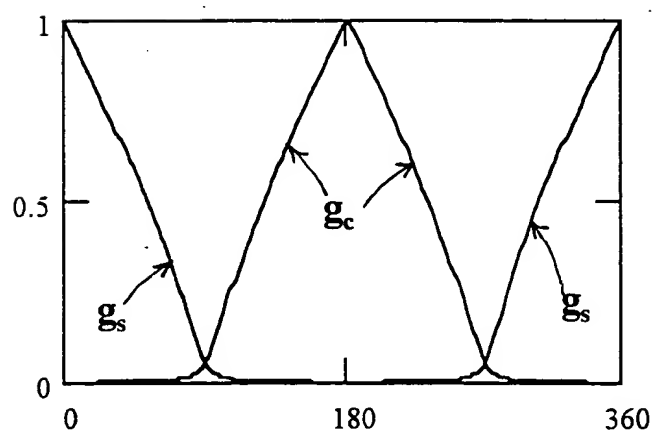


FIG. 8

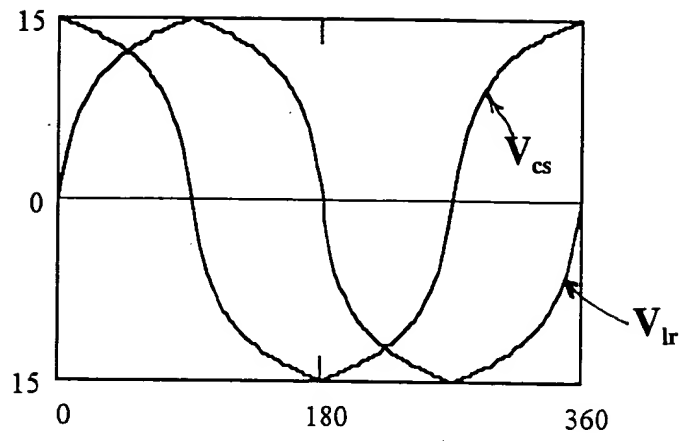


FIG. _9

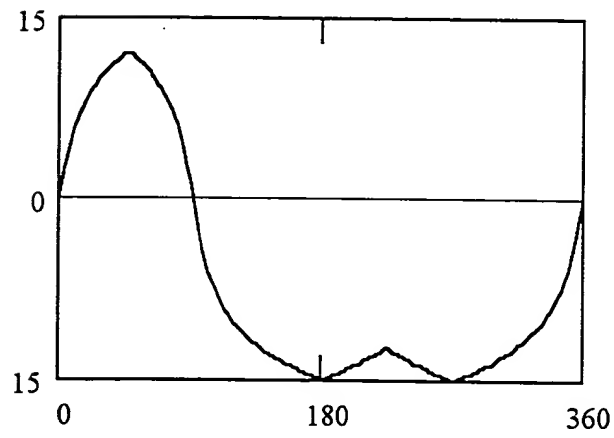


FIG. _10

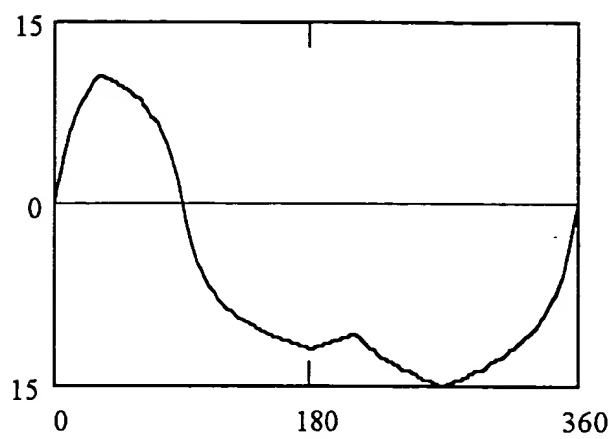


FIG. 11

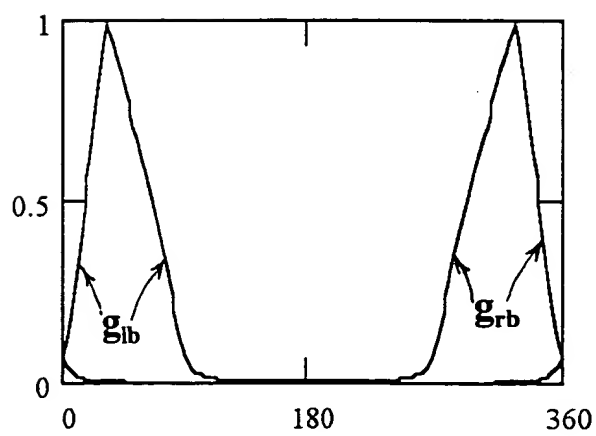


FIG. 12

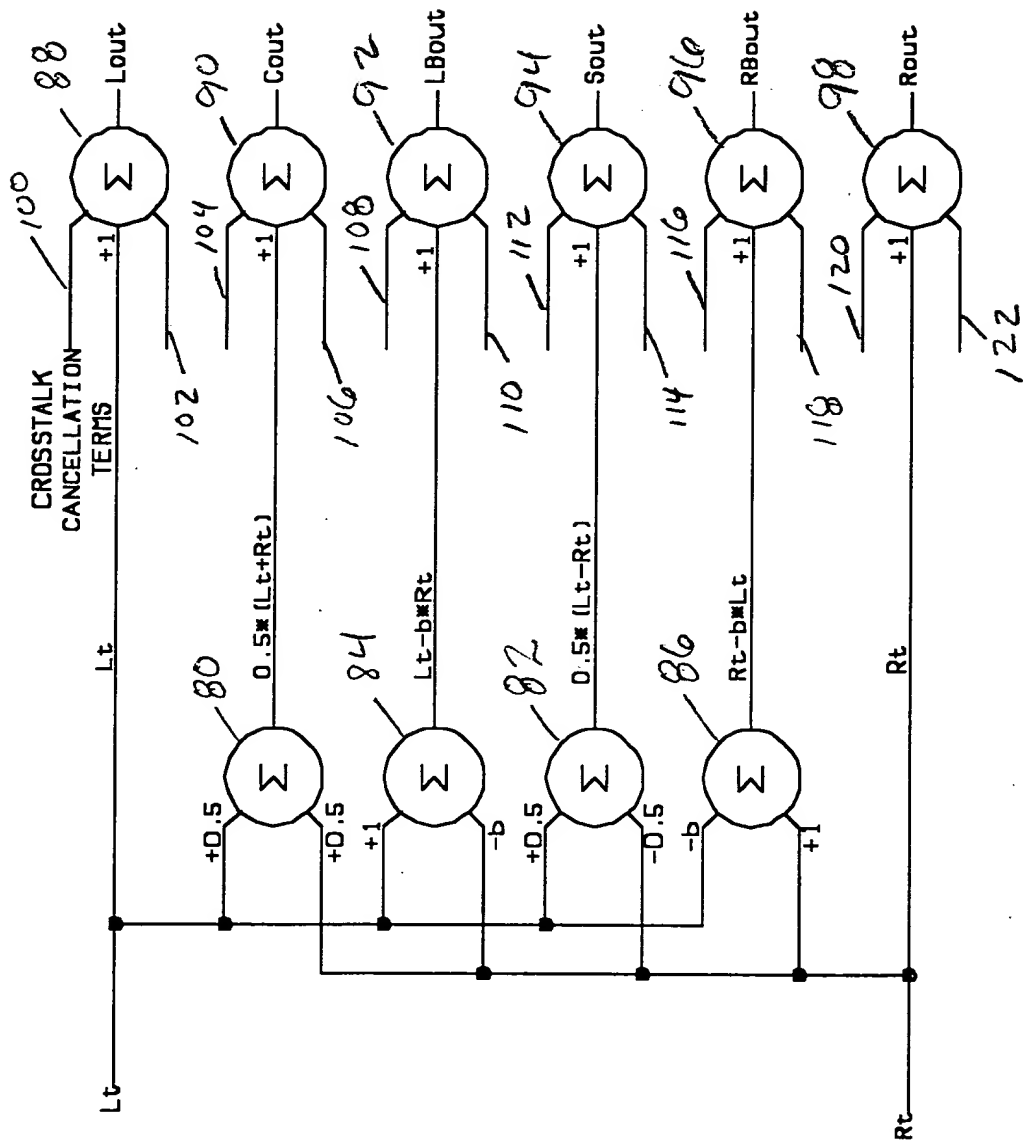


FIG. 13

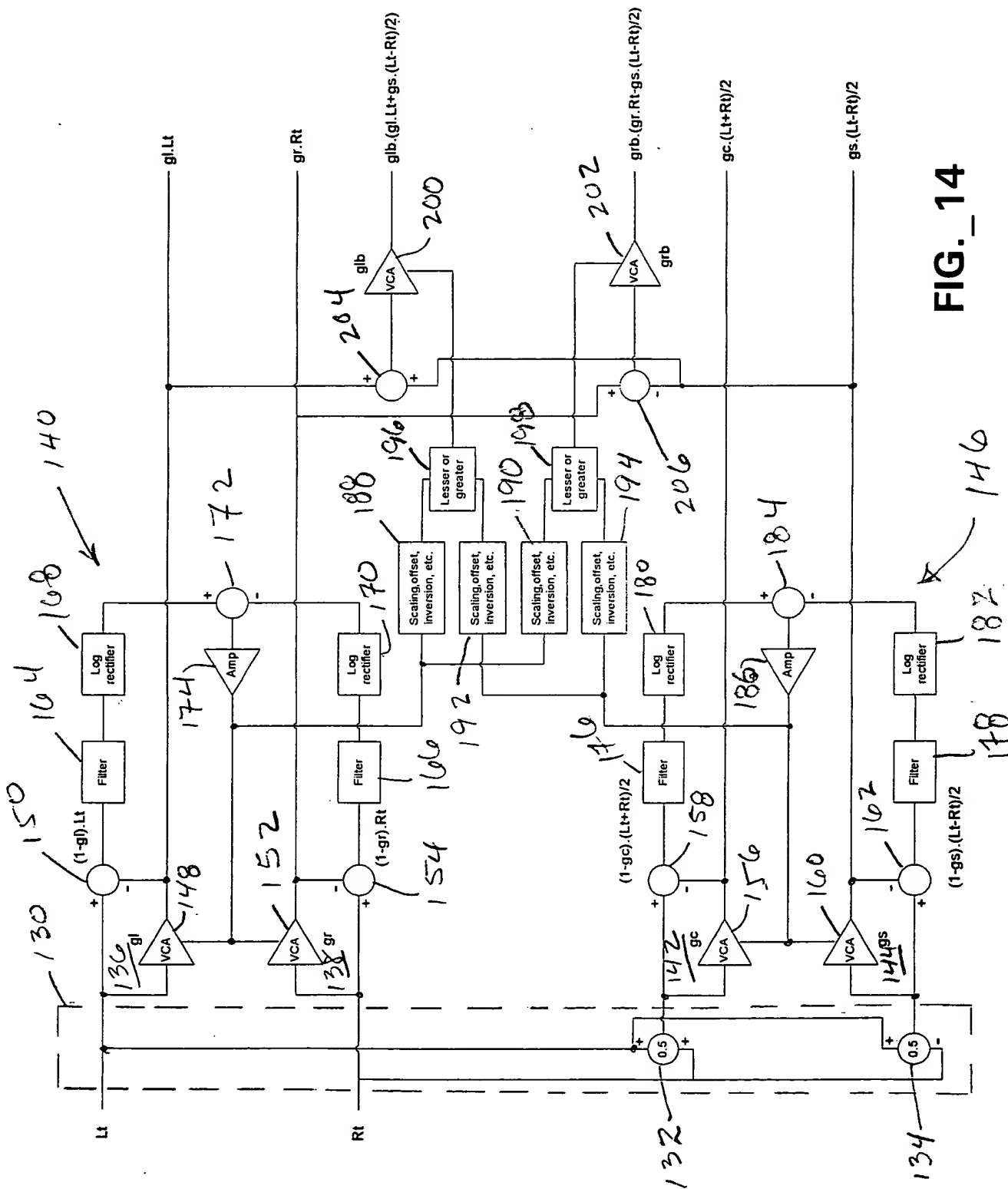


FIG. 14

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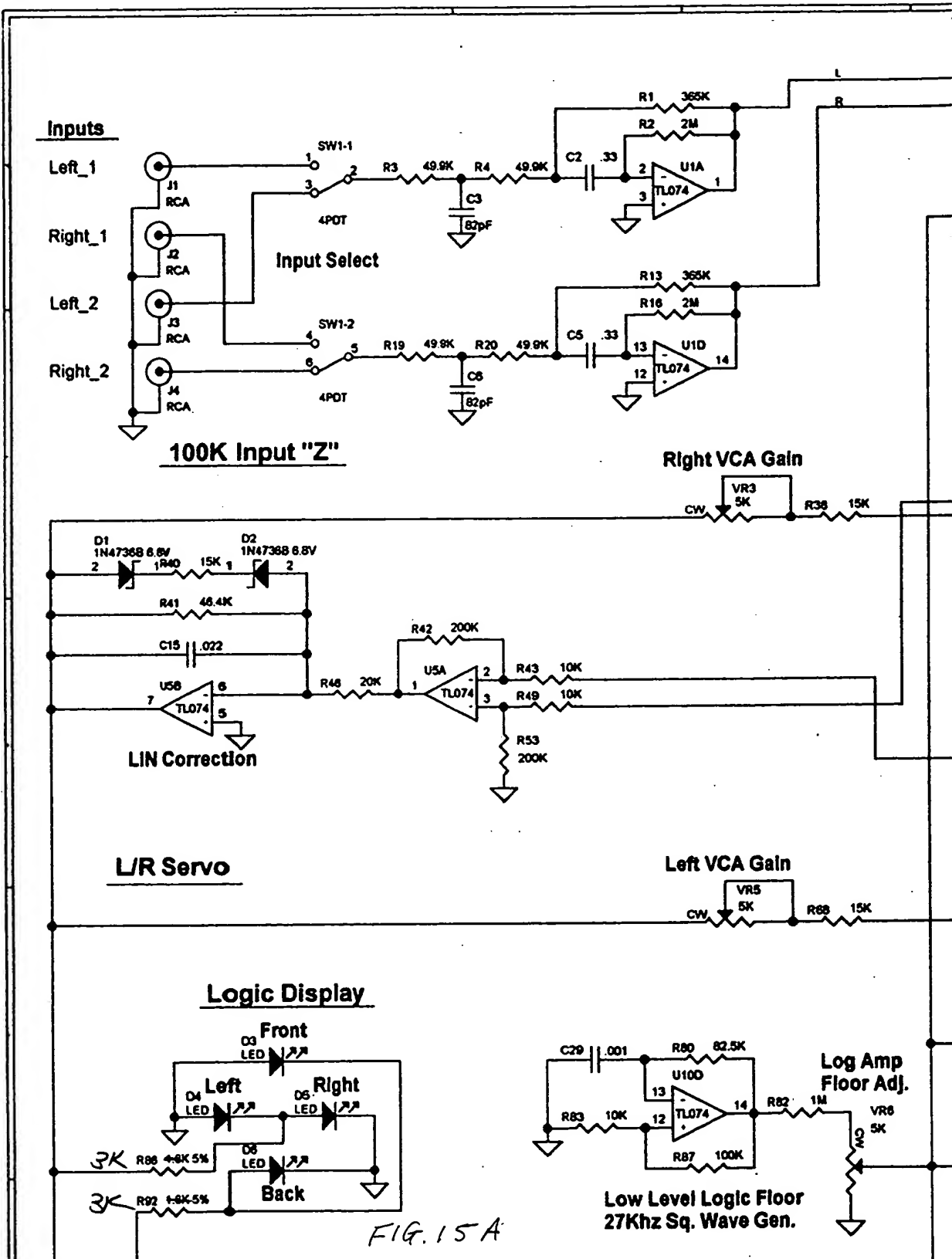
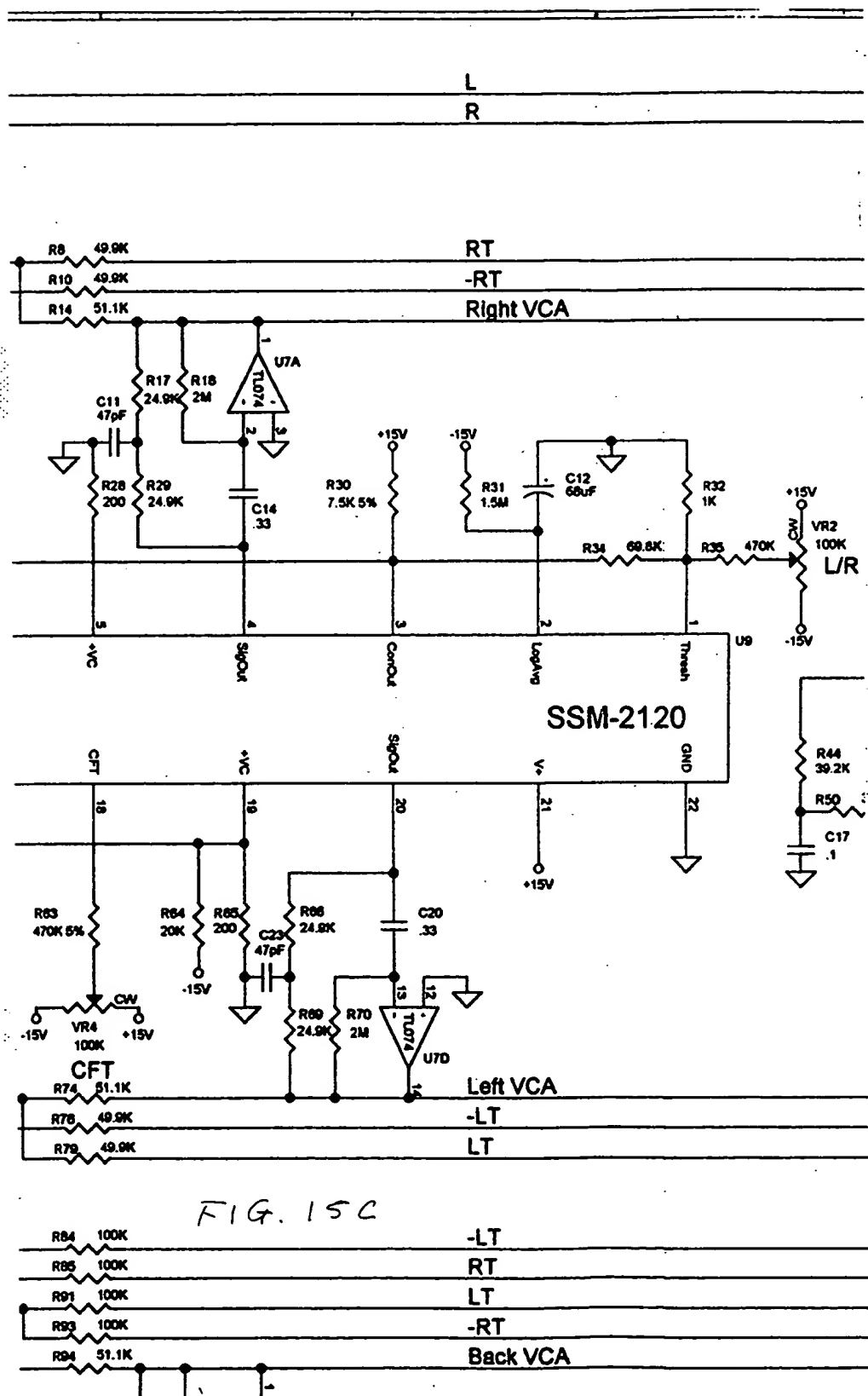


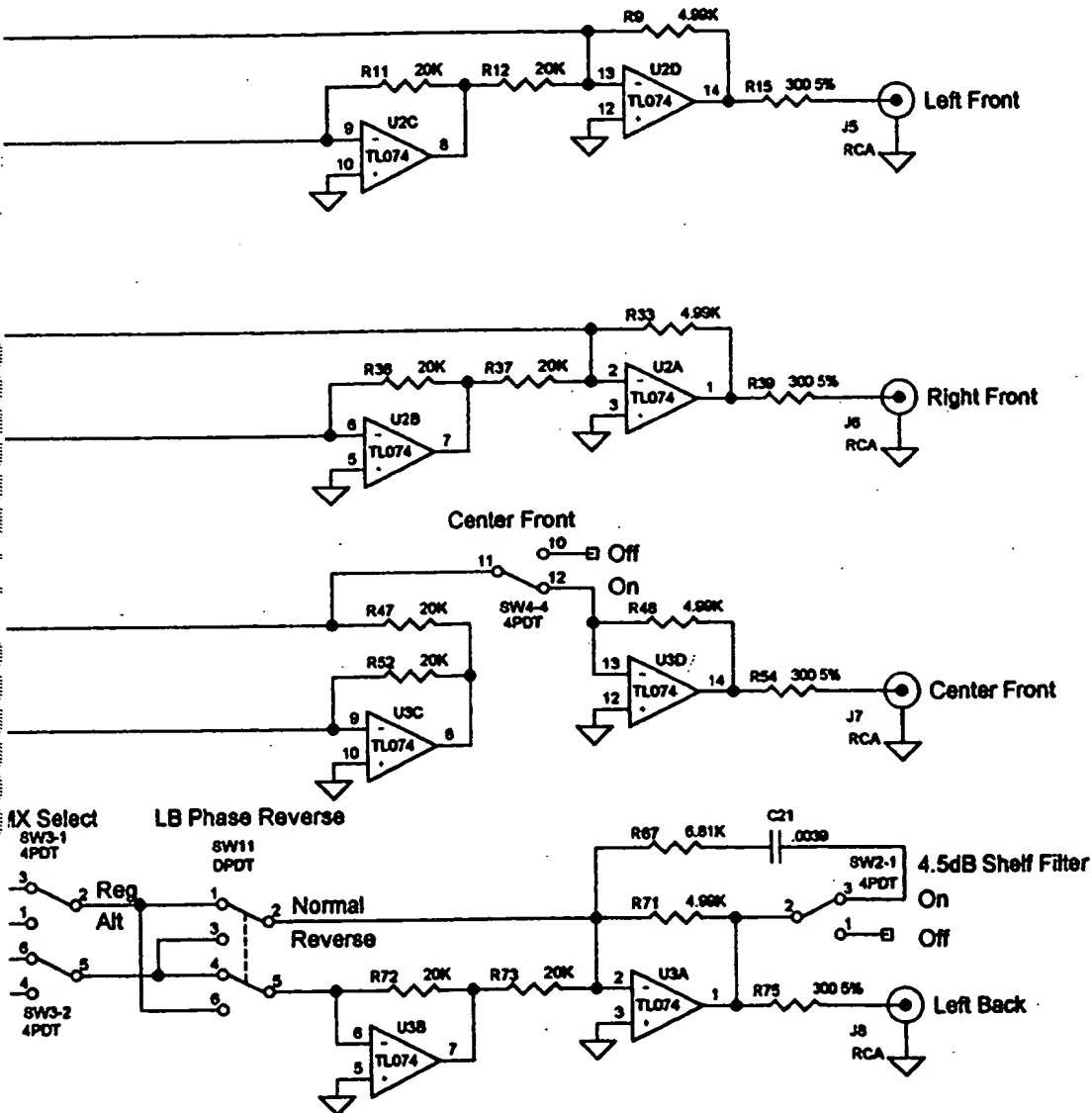
FIG. 15A

7. / 2



[illegible]

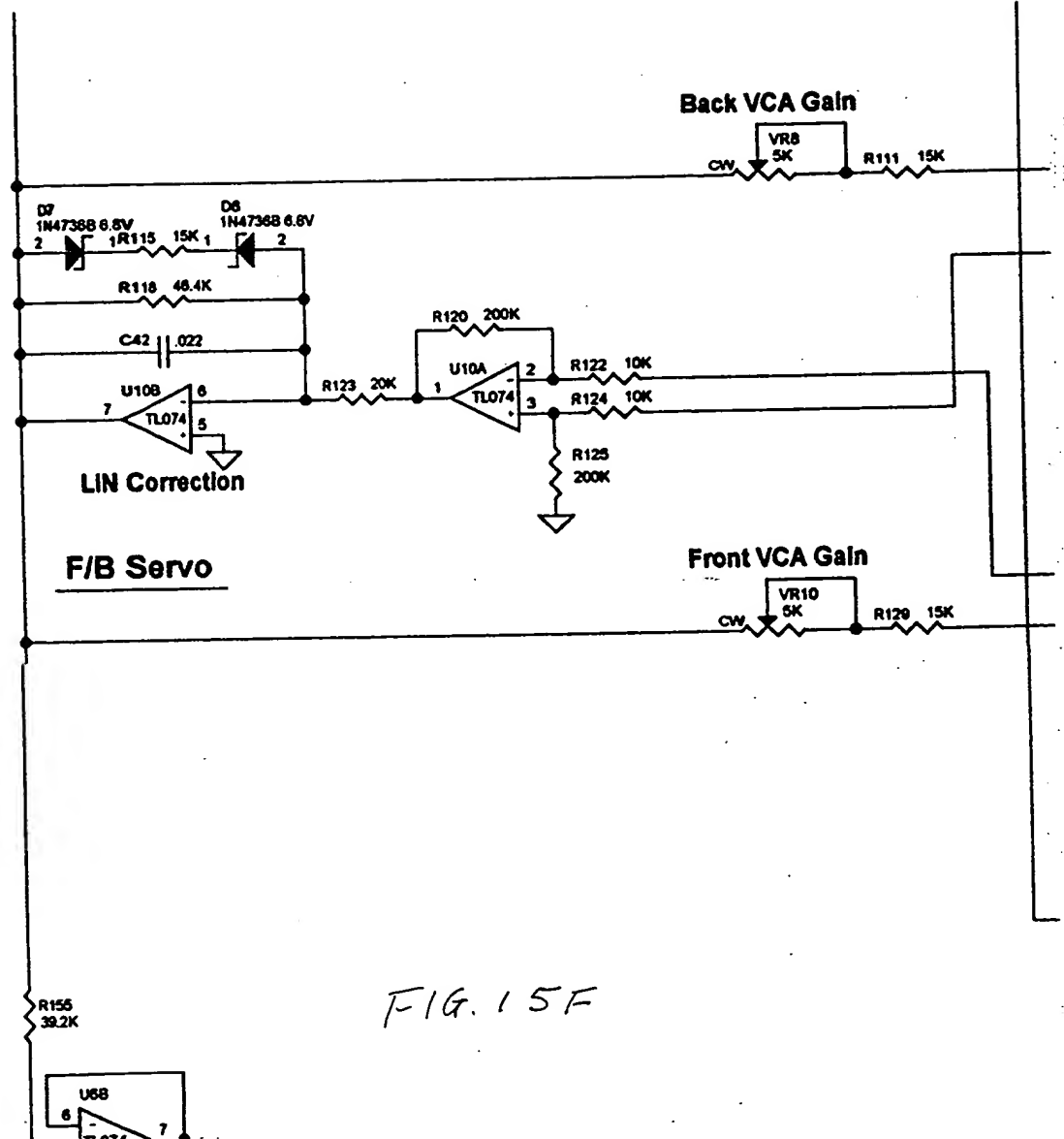
Outputs



MX Select Alt is for
3 Back Channel
Version

FIG. 15E

00000-1722500



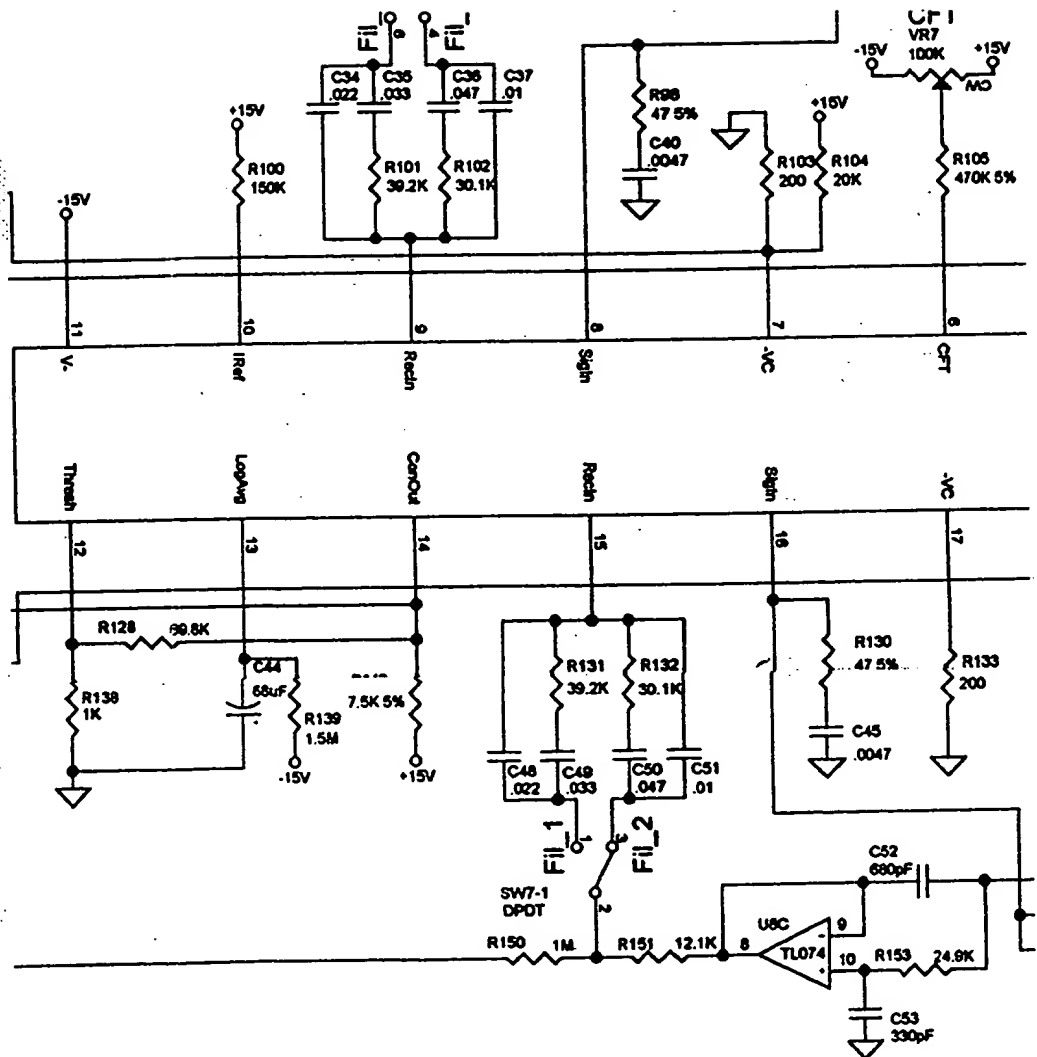


FIG. 15G

The diagram illustrates the internal circuitry of the SSM-2120 signal processor. It features two operational amplifiers, U8A (TL074) and U8D (TL074), and a variable resistor U11 (VR9, 100K). The circuit is powered by +15V and -15V rails. Key components include resistors R108 (7.5K 5%), R109 (1.5M), R110 (1K), R116 (69.8K), R117 (470K), R134 (470K 5%), R135 (20K), R136 (200), R137 (24.9K), R142 (24.9K), R143 (2M), R145 (51.1K), R149 (100K), R152 (100K), R154 (100K), R156 (100K), R157 (34.8K), and R158 (34.8K). Capacitors C38 (47pF), C41 (.33), C43 (.33), and C46 (47pF) are also present. The circuit includes several input and output pins: +V, SigOut, ComOut, LogAns, Thrash, GND, CFT, and +15V. The output section includes a variable resistor U11 (VR9, 100K) and a 100K resistor VR9. The diagram is labeled FIG. 15H.

FIG. 15H

FIG. 15I

FIG. 15I

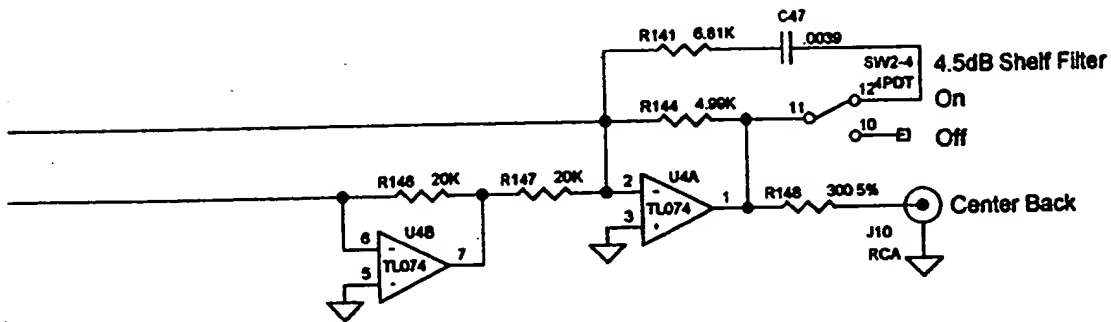
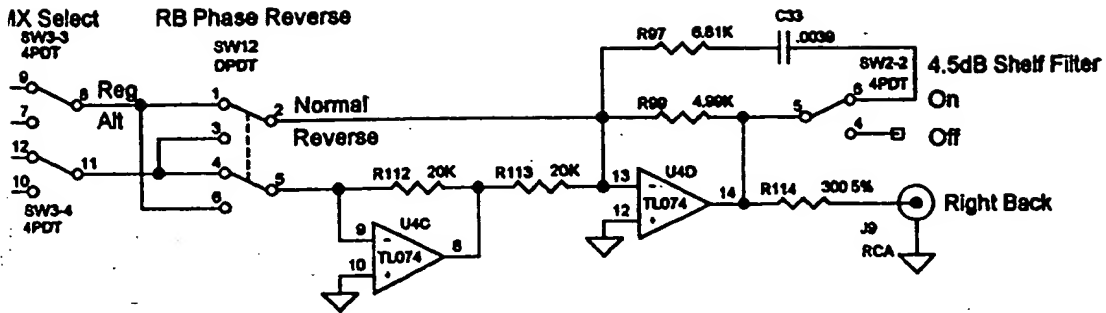
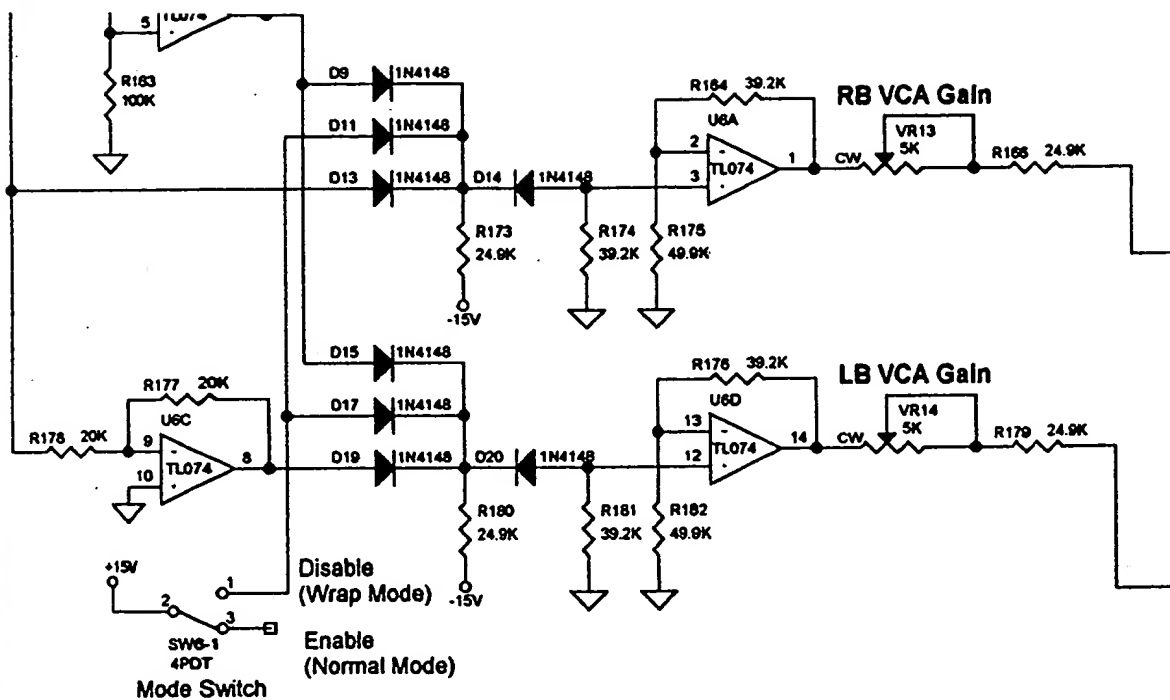


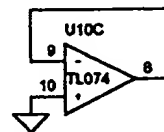
FIG. 15J

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Back Corner Logic

FIG. 15K



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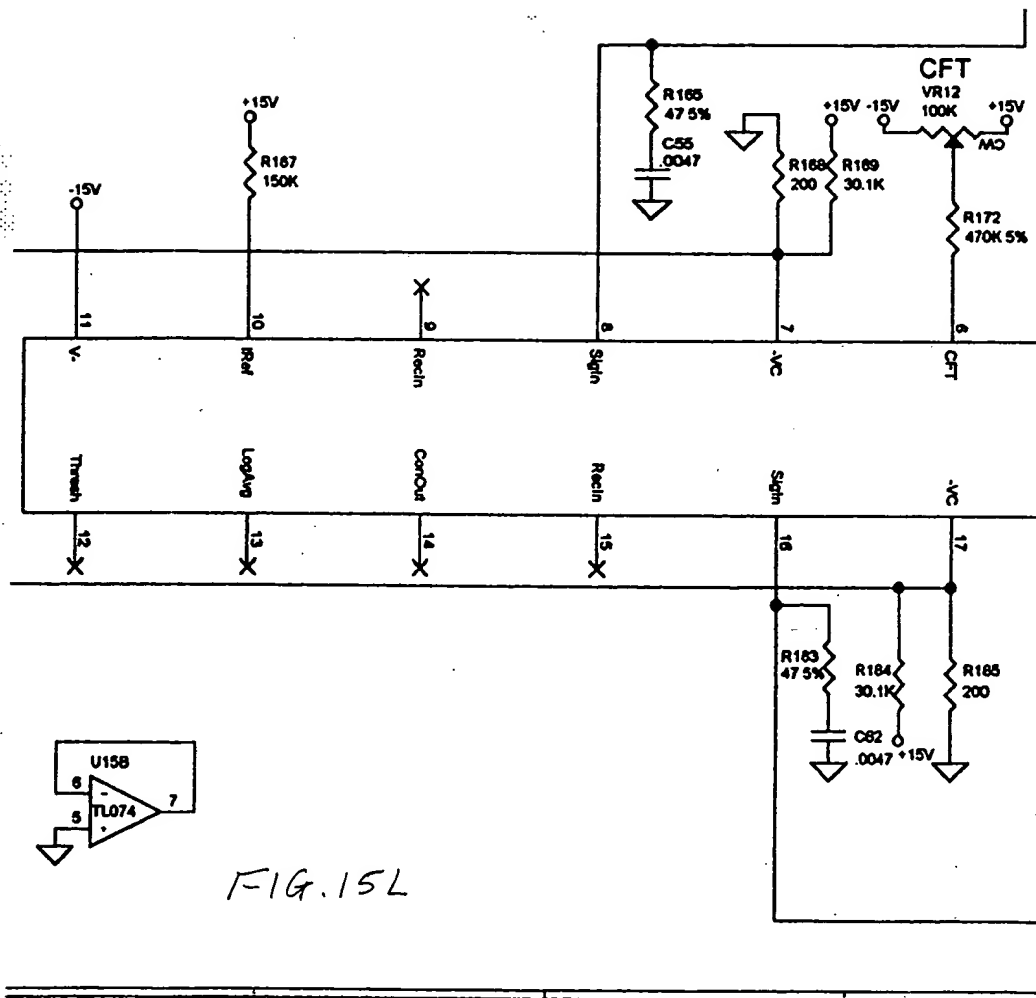
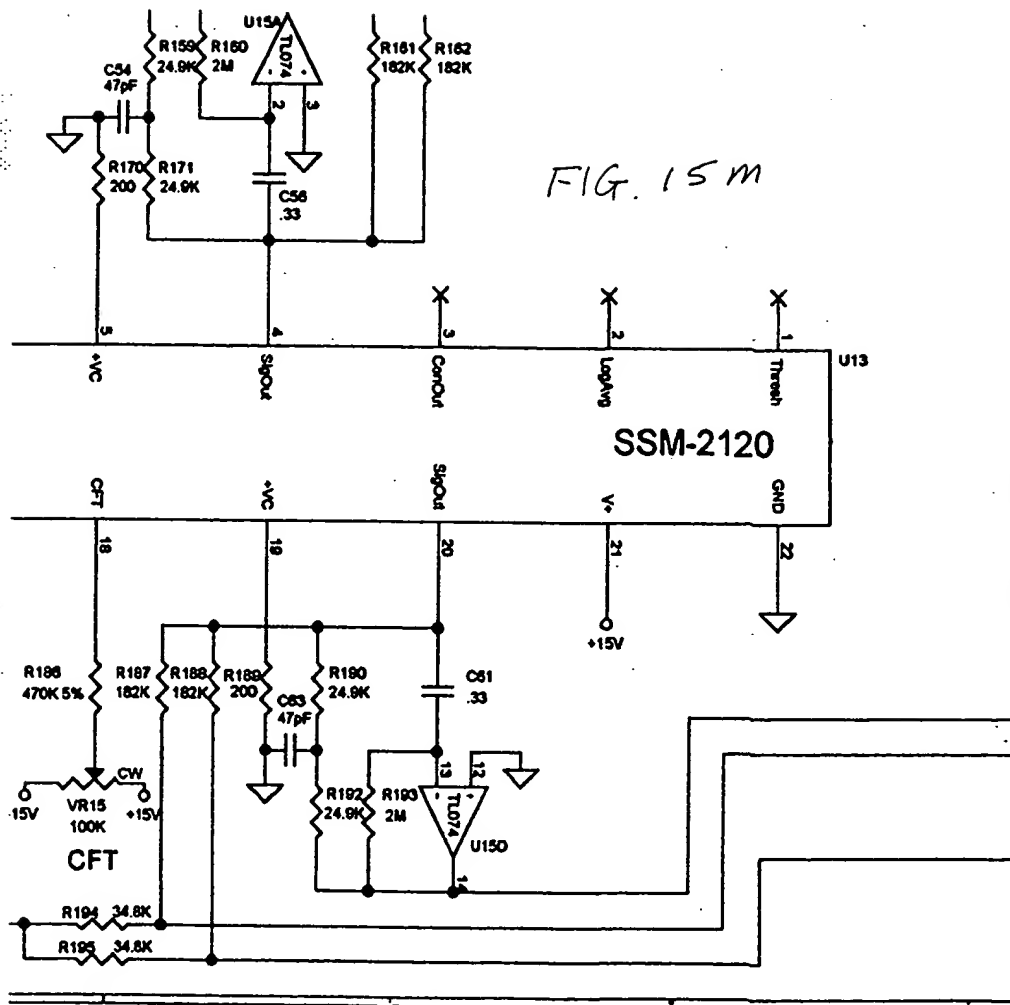


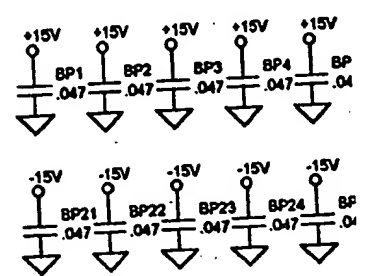
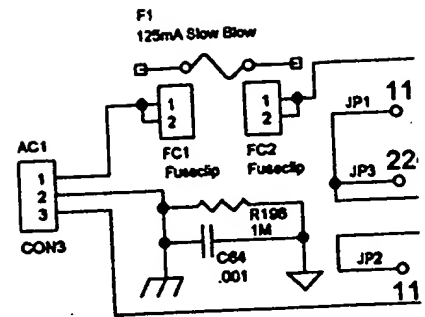
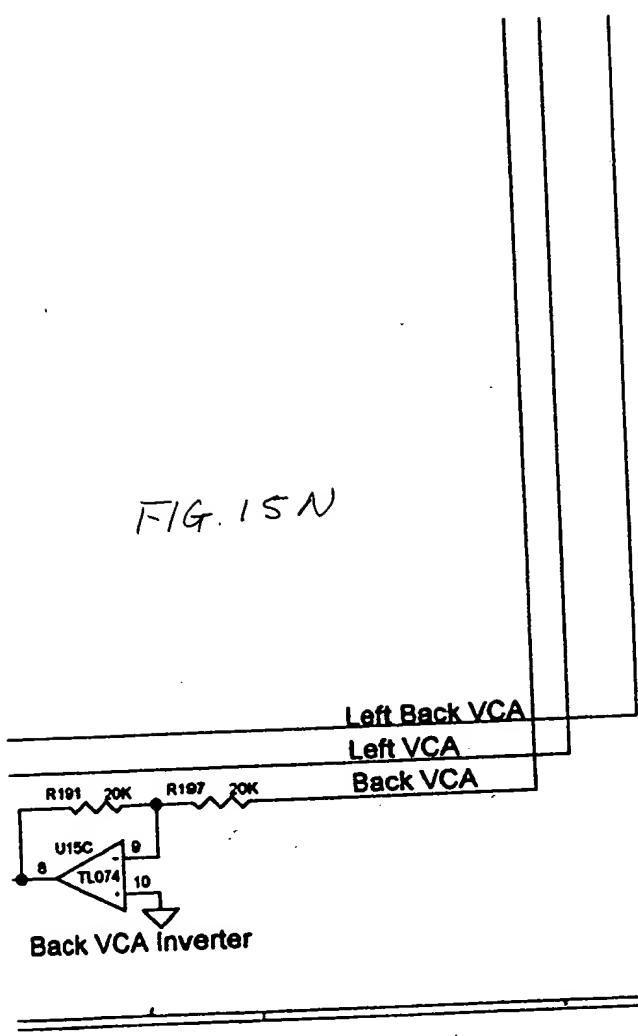
FIG. 15L

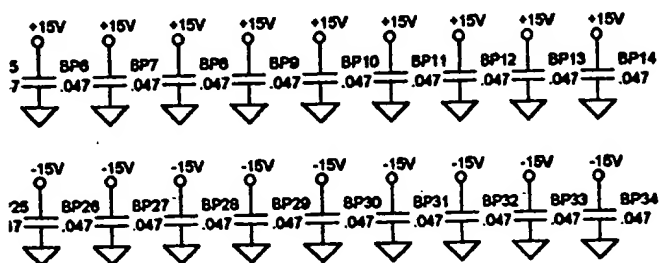
0063244-032000



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FIG. 15N





Title			
6-Channel Dolby Surround			
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